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Frank Vahid is a professor and author. Other books by Frank Vahid include Verilog for Digital Design, Digital System Design and Programming Embedded Systems: An Introduction to Time-Oriented Programming. Frank Vahid is a Professor at the Department of Computer Science and Engineering, in the College of Engineering, University of California.

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This is the first book on embedded systems to offer a unified approach to hardware and software specification and design issues -- and the first to outline a new specify- explore-refine paradigm that is presently being used in industry in an ad-hoc manner, but until now has not been formally described.

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